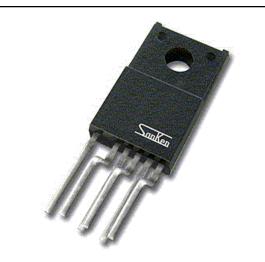
## Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator



#### **ABSOLUTE MAXIMUM RATINGS** at $T_A = +25$ °C

Control Supply Voltage, V <sub>CC</sub> <b>35 V</b>
Drain-Source Voltage, V <sub>DSS</sub> <b>650 V</b>
Drain Switching Current, I <sub>D</sub> 11.2 A*
Peak Drain Switching Current,
I <sub>DM</sub> 11.2 A
Single-Pulse Avalanche Energy,
E <sub>AS</sub>
OCP/BD Voltage Range,
V <sub>OCP</sub> 1.5 V to +5 V
FB Input Current, I <sub>FB</sub> 10 mA
FB Voltage Range, V <sub>FP</sub> -0.5 V to +9 V
FB Voltage Range, V <sub>FP</sub> <b>-0.5 V to +9 V</b> Package Power Dissipation. P <sub>D</sub>
Package Power Dissipation, P <sub>D</sub>
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)})$ 0.8 W
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph MOSFET Channel Temp., $T_J$ . +150°C
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph MOSFET Channel Temp., $T_J \dots +150^{\circ}\text{C}$ Internal Frame Temp., $T_F \dots +115^{\circ}\text{C}$
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph MOSFET Channel Temp., $T_J \dots +150^{\circ}\text{C}$ Internal Frame Temp., $T_F \dots +115^{\circ}\text{C}$ Operating Temperature Range,
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph MOSFET Channel Temp., $T_J \dots +150^{\circ}\text{C}$ Internal Frame Temp., $T_F \dots +115^{\circ}\text{C}$ Operating Temperature Range, $T_A \dots -20^{\circ}\text{C}$ to $+115^{\circ}\text{C}\dagger$
Package Power Dissipation, $P_D$ control $(V_{CC} \times I_{CC(ON)}) \dots 0.8 \text{ W}$ MOSFET $(V_{DSS} \times I_D) \dots$ See Graph MOSFET Channel Temp., $T_J \dots +150^{\circ}\text{C}$ Internal Frame Temp., $T_F \dots +115^{\circ}\text{C}$ Operating Temperature Range,

\* Drain switching current is limited by temperature (page 2) and safe operating area (page 5).

†For the availability of parts meeting -40°C requirements, contact Allegro's Sales Representative.

The STR-W6753 is a quasi-resonant regulator specifically designed to satisfy the requirements for increased integration and reliability in switch-mode power supplies. It incorporates a primary control and drive circuit with an avalanche-rated power MOSFET. The regulator exhibits only low-level high-frequency EMI noise because of soft switching of the MOSFET close to ground (bottom point). A bottom-skip function minimizes an increase of operational frequency during light loads to improve system efficiency over the entire load range.

Covering the power range from below 120 watts for a 230 VAC input, or 58 watts for a universal input (85 to 264 VAC), this device can be used in a range of applications, from DVD and VCR players to ac adapters for cellular phones and digital cameras. An auto-standby function, which is internally triggered by sensing on time, reduces power consumption at light load. An externally triggered standby mode reduces the input power further. Multiple protections, including the avalanche-energy-guaranteed MOSFET, provide high reliability of system design. Devices with an increased output power rating are the STR-W6754 and STR-W6756.

Cycle-by-cycle current limiting, undervoltage lockout with hysteresis, and overvoltage protection protect the power supply during the normal overload and fault conditions. Overvoltage protection is latched after a short delay. The latch may be reset by cycling the input supply. Low start-up current and a low-power standby mode selected from the secondary circuit completes a comprehensive suite of features. The STR-W6753 is provided in a fully molded TO-220-style flangemounted, high power, isolated plastic package.

#### FEATURES AND BENEFITS

- Rugged 650 V Avalanche-Rated MOSFET Simplified Surge Absorption No V<sub>DSS</sub> Derating Required
- $\blacksquare$  1.7  $\Omega$  Maximum  $\mathbf{r}_{DS(on)}$
- Two Operational Modes by Automatic Switching: Quasi-Resonant Mode for Normal Operation Burst Mode for Standby Operation or Light Loads
- Automatic or Manually Triggered Burst Standby Input Power < 0.1 W at No Load
- Low Operating Current (6 mA typ)

— continued

Always order by complete part number, e.g., |STR-W6753|.

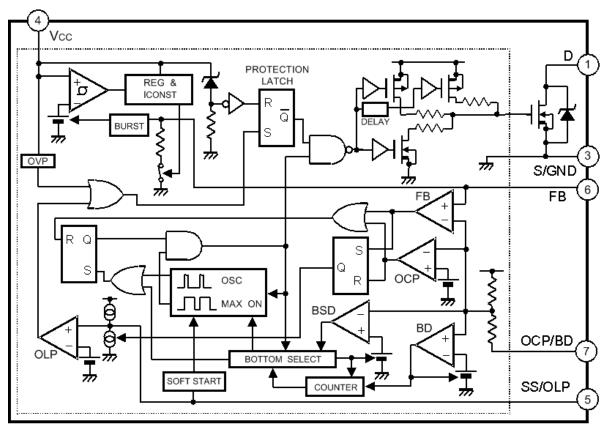


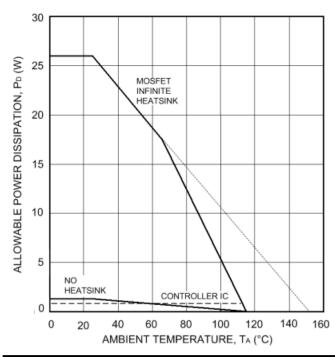


## Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator

Switching Regulators

#### **FUNCTIONAL BLOCK DIAGRAM**





#### FEATURES AND BENEFITS (cont'd)

- Auto-Bias Function
  Stable Burst Operation Without Generating Interference
- Internal Off-Timer Circuit
- Built-In Constant-Voltage Drive
- Multiple Protections:
  Pulse-by-Pulse Overcurrent Protection
  Overload Protection with Auto Recovery
  Latching Overvoltage Protection
  Undervoltage Lockout with Hysteresis
- RoHS Compliant





Switching Regulators

### STR-W6753 Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator

## ELECTRICAL CHARACTERISTICS at $T_A = +25$ °C, $V_{CC} = 20$ V, voltage measurements are referenced to S/GND terminal (unless otherwise specified).

				Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units	
Start-Up Operation			-				
Operation Start Voltage	V <sub>CC(ON)</sub>	Turn-on, V <sub>CC</sub> = 0 → 19.9 V	16.3	18.2	19.9	V	
Soft-Start Operation Stop Voltage	V <sub>SS/OLP</sub>		1.1	1.2	1.4	V	
Soft-Start Oper. Charging Current	I <sub>SS/OLP</sub>		-390	-550	-710	μΑ	
Operation Stop Voltage	V <sub>CC(OFF)</sub>	Turn-off, V <sub>CC</sub> = 19.9 → 8.8 V	8.8	9.7	10.6	V	
Circuit Current in Non-Operation	I <sub>CC(OFF)</sub>	V <sub>CC</sub> = 15 V	_	_	100	μΑ	
Normal Operation							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 300 μA	650	_	_	V	
Drain Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> = 650 V	_	_	300	μΑ	
On-State Resistance	r <sub>DS(on)</sub>	I <sub>D</sub> = 1.4 A, T <sub>J</sub> = +25°C	_	_	1.7	Ω	
Switching Time	t <sub>f</sub>		_	_	400	ns	
Circuit Current	I <sub>CC(ON)</sub>		_	_	6.0	mA	
Oscillation Frequency	f <sub>osc</sub>		19	22	25	kHz	
Bottom-Skip Oper. Threshold Volt.	V <sub>OCPBD(BS1)</sub>		-605	-665	-720	mV	
	V <sub>OCPBD(BS2)</sub>		-385	-435	-485	mV	
Quasi-Resonant Oper. Threshold	V <sub>OCPBD(TH1)</sub>		280	400	520	mV	
	V <sub>OCPBD(TH2)</sub>		670	800	930	mV	
Feedback-Pin Threshold Voltage	V <sub>FB(OFF)</sub>		1.32	1.45	1.58	V	
Feedback-Pin Current	I <sub>FB(ON)</sub>		600	1000	1400	μΑ	
Standby Operation							
Standby Operation Start Voltage	V <sub>CC(S)</sub>	V <sub>CC</sub> = 0 → 12.2 V	10.3	11.1	12.1	V	
Standby Oper. Start Volt. Interval	V <sub>CC</sub>		1.10	1.35	1.65	V	
Standby Non-Operation Current	I <sub>CC(S)</sub>	V <sub>CC</sub> = 10.2 V	_	20	56	μΑ	
Feedback-Pin Current	I <sub>FB(ON)</sub>	V <sub>CC</sub> = 10.2 V	_	4.0	14	μΑ	
Feedback-Pin Threshold Voltage	V <sub>FB(S)</sub>	V <sub>CC</sub> = 12.2 V	0.55	1.10	1.50	V	
Minimum ON Time	t <sub>on(min)</sub>		0.5	0.8	1.2	μs	

continued next page ...

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### Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator



## ELECTRICAL CHARACTERISTICS at $T_A = +25$ °C, $V_{CC} = 20$ V, voltage measurements are referenced to S/GND terminal (unless otherwise specified).

			Limits							
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units				
Protection Operation										
OVP Operation Voltage	V <sub>CC(OVP)</sub>	Turn-off, $V_{CC} = 0 \rightarrow 29.9 \text{ V}$	25.5	27.7	29.9	V				
Maximum ON Time	t <sub>on(max)</sub>		27.5	32.5	39.0	μs				
OLP Operation Voltage	V <sub>SSOLP</sub>		4.0	4.9	5.8	V				
OLP Operation Current	I <sub>SSOLP</sub>		-6.0	-11	-16	μA				
Overcurrent Detect. Threshold Volt.	V <sub>OCPBD(LIM)</sub>		-0.895	-0.940	-0.995	V				
OCP/BD-Pin Current	I <sub>OCPBD</sub>		-40	-100	-250	μA				
Latch Holding Current	I <sub>CC(H)</sub>	$V_{CC} = 29.9 \rightarrow V_{CC(OFF)} - 0.3 \text{ V}$	-	45	140	mA				
Latch Release Voltage	V <sub>CC(L)</sub>	V <sub>CC</sub> = 29.9 → 6 V	6.0	7.2	8.5	V				
Other										
Thermal Resistance	$R_{ hetaJF}$	Output junction-to-frame	_	_	2.0	°C/W				

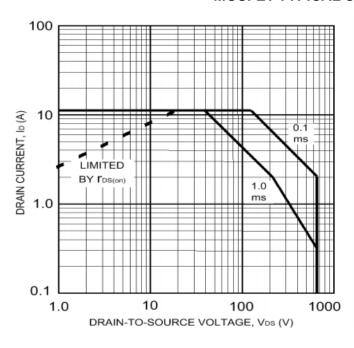
NOTES: 1. Typical Data is for design information only.

2. Negative current is defined as coming out of (sourcing) the specified device termninal.

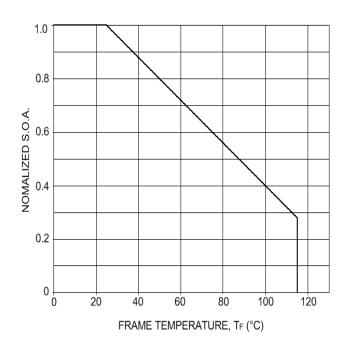


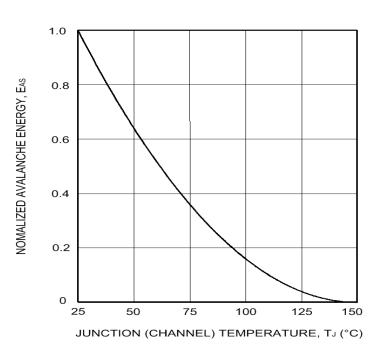
## Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator

#### **MOSFET TYPICAL CHARACTERISTICS**



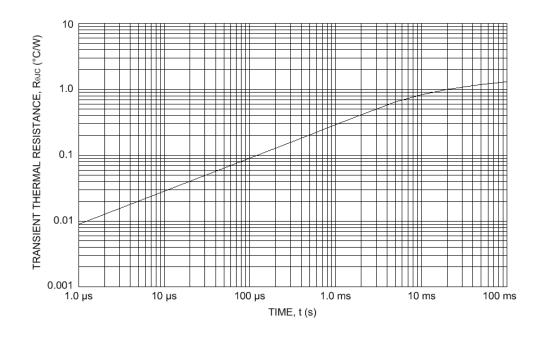
Avalanche energy is measured at  $V_{DD}$  = 99 V, L = 20 mH,  $I_L$  = 3.6 A.





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#### **MOSFET TYPICAL CHARACTERISTICS (cont'd)**



**WARNING** — These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

The use of an isolation transformer is recommended during circuit development and breadboarding.



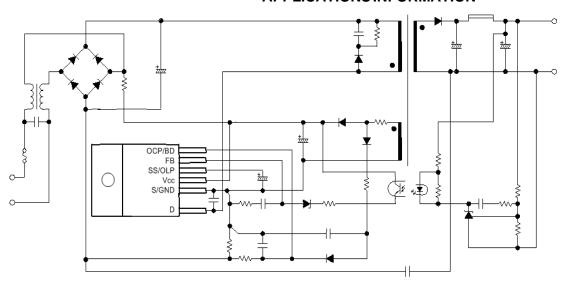


Switching Regulators

#### STR-W6753

### Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator

#### **APPLICATIONS INFORMATION**



**Typical Application** 

Complete product description and applications information is provided in Application Note 28103.30, *Series STR-W6750 Off-Line Quasi-Resonant Flyback Switching Regulators*.

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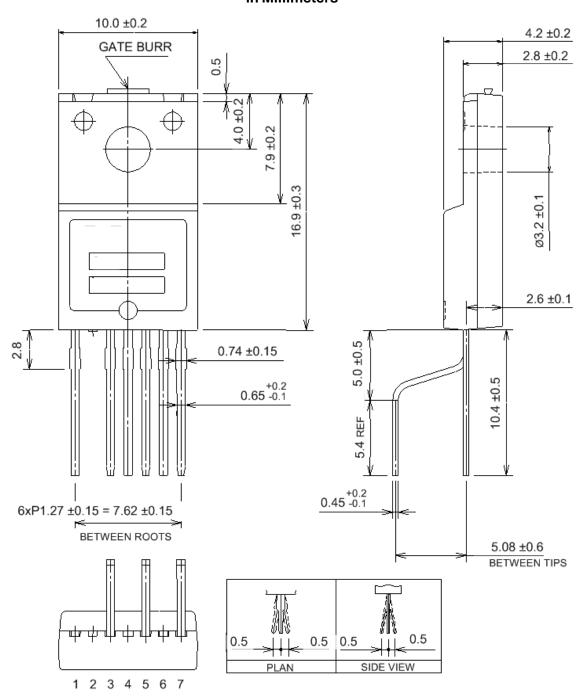
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## Universal-Input/58 W Off-Line Quasi-Resonant Flyback Switching Regulator

# Switching Regulators

## PACKAGE DIMENSIONS in Millimeters



Product weight: approx. 2.3 g.

Recommended mounting hardware torque: 0.588 ~ 0.785 Nm, 6 ~ 8 kgf x cm.

Recommended silicon grease: Dow Corning SC102, Toshiba YG6260, Shin-Etsu G746, or equivalent.



